



41st Conference on Design of Circuits and Integrated Systems

18-20 November 2026, Ericeira, Portugal

CALL FOR PAPERS

The conference on Design of Circuits and Integrated Systems (DCIS) (www.dcis2026.com) is an international meeting for researchers in the highly active fields of micro- and nano-electronic circuits and integrated systems. It provides an excellent forum to present and discuss works on the emerging challenges offered by technology, in the areas of modeling, design, implementation and test of devices, circuits and systems.

DCIS 2026 topics of interest include:

- Analog / mixed signal circuits and systems
- Design of power-, thermal- and variability-aware circuits and systems
- New computing and hardware paradigms (machine learning, approximate and stochastic computing, bio-inspired computing, ...)
- Circuits and systems in advanced and emerging technologies (SiC, GaN, silicon photonics, 3DICs, quantum devices, ...)
- Reconfigurable devices and systems (FPGA, NVRAM, memristors, ...)
- Embedded and high-performance computing
- EDA tools and methods
- Industrial and power electronics
- Sustainable computing and systems
- Sensory circuits and systems
- Energy management and harvesting
- Test, fault tolerance, reliability and modeling
- Radiofrequency ICs (6G, RFID, NFC, ...)
- On-Chip and off-chip interconnects
- IoT and applications (industry 4.0, personalized healthcare, ...)
- Hardware security
- Educational Methods on Electronics

We encourage submission of manuscripts (IEEE single-spaced, double-column, A4 page, 6 pages maximum) employing the IEEE manuscript templates for conference proceedings through the online paper submission system at the conference website.

Important Dates

Deadline for submission: May 17th, 2026
Notification of acceptance: July 20th, 2026
Deadline for final paper submission: September 20th, 2026

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Venue

DCIS 2026 will be held at Hotel Vila Galé
Ericeira, Ericeira, Portugal

Contacts: Fábio Passos

fabio.passos@tecnico.ulisboa.pt

Organized by:

